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CONFIRMATION NO. 9683

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## APPLICANTS

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## \*\* CONTINUING DATA \*\*\*\*\*

This application is a DIV of 10/107,568 03/27/2002 PAT 6,765,297 *Yes* *De*

## \*\* FOREIGN APPLICATIONS \*\*\*\*\*

JAPAN P2001-090292 03/27/2001 *Yes* *De*

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

\*\* 11/20/2003

Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR COUNTRY JAPAN	SHEETS DRAWING 10	TOTAL CLAIMS 9	INDEPENDENT CLAIMS 3
35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance				
Verified and Acknowledged <i>De</i> Examiner's Signature	<i>De</i> Initials			

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## TITLE

Semiconductor device and wiring forming method in semiconductor device

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